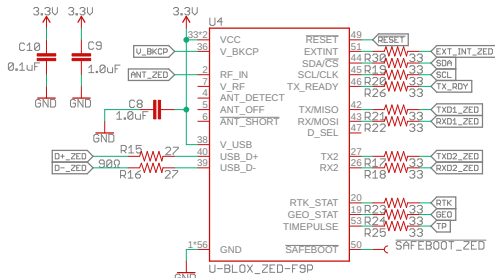


### GNSS Module: u-blox ZED-F9P

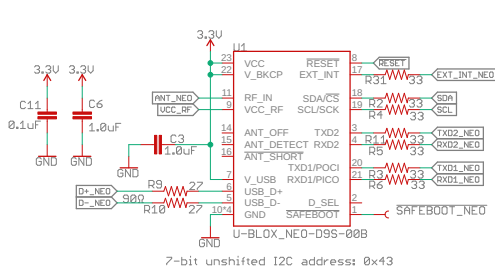
VCC Ranges: 3.0 - 3.6V



7-bit unshifted I2C address: 0x42

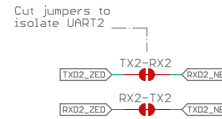
### L-Band Corrections Module: u-blox NEO-D9S

VCC Ranges: 3.0 - 3.6V



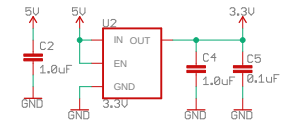
7-bit unshifted I2C address: 0x43

### UART2 Link for PMP Corrections ZED-F9P <-> NEO-D9S

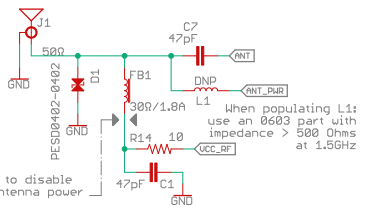


### Voltage Regulation - AP2112K

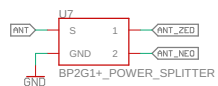
I<sub>out</sub> (max): 600mA  
V<sub>in</sub> (max): 6.5V  
V<sub>drop</sub> (max): 250mV  
I<sub>q</sub>: 55uA



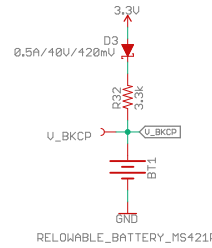
### L1/L2/L-Band Antenna



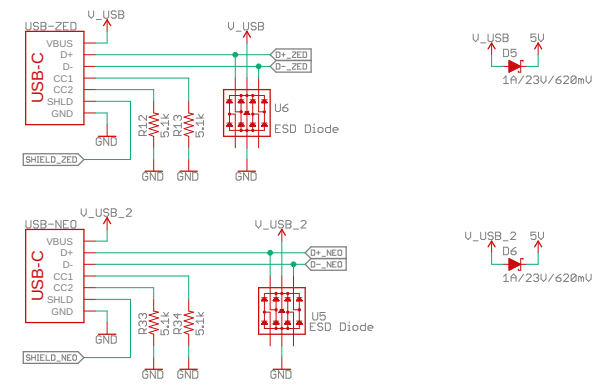
### Antenna Divider - BP2G1+



### Backup Battery



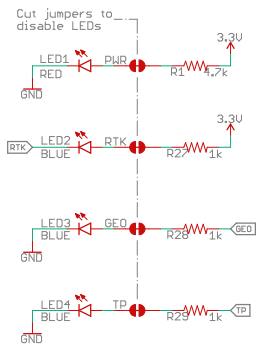
### USB



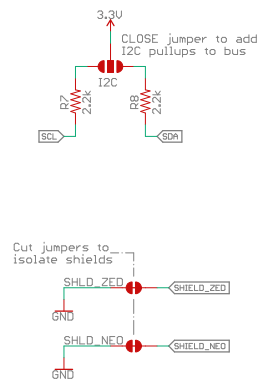
RF Track Impedances Coplanar Waveguide with Ground Calculations  
<https://chemandy.com/calculators/coplanar-waveguide-with-ground-calculator.htm>  
 Ground is on layer 2. Prepreg thickness: 0.2mm, Er = 4.6  
 12.5 mil track with 5 mil gap = 50 Ohms

USB Track Impedance: Differential Pair  
<https://saturnpcb.com/saturn-pcb-toolkit/>  
 Prepreg thickness: 0.2mm (7.1 mil), Er = 4.6  
 8 mil track with 5 mil gap (13 mil center to center) = 90 Ohms

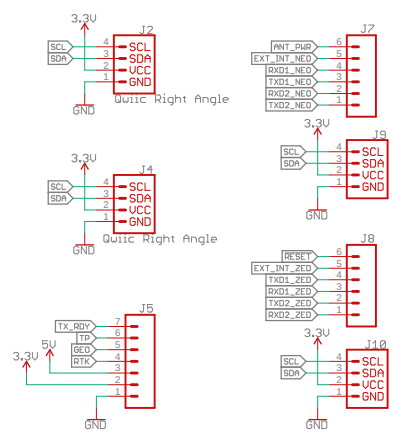
### LEDs



### Jumpers



### Headers



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TITLE: ZED-F9P\_NEO-D9S\_Combo

Design by: Paul Clark  
 Revised By: Elias Santistevan

Date: 6/15/2023 11:12 AM

REU: v10  
 Sheet: 1/1